





#### **Customer Part:**

### Description

 Surface mount Voltage Controlled Temperature Compensated Oscillator (VCTCXO) providing a high degree of frequency stability over a wide temperature range in a hermetically sealed ceramic package

Model CFPT-126Model Issue number 5

### **Frequency Parameters**

Frequency
 Frequency Stability
 Operating Temperature Range
 40.00 to 85.00°C

Ageing ±1ppm typical in 1st year @

25°C

Frequency Stability Reference to (Fmax + Fmin)/2

### **Electrical Parameters**

Supply Voltage 3.3V ±5%
 Current Draw (Typical): 3mA @ 20MHz

Supply Voltage Variation:
 <30MHz ±0.3ppm</li>
 30MHz to 40MHz ±0.4ppm

Load Variation: ±0.2ppm (@15pF ±10%)

After Reflow: ±1.0ppm

## **Frequency Adjustment**

Pulling ±5ppm minControl Voltage 1.65V ±1.0V

### **Output Details**

Output Compatibility
 Drive Capability
 Rise and Fall Time
 Duty Cycle
 HCMOS
 15pF nom
 8.0ns max
 45/55%

### **Environmental Parameters**

- Shock: IEC 60068-2-27, Test Ea: 980m/s2 acceleration for 6ms, 3 shocks in each of 3 mutually perpendicular planes
- Vibration: IEC 60068-2-6, Test Fc, Procedure B4: 10Hz-60Hz
   1.5mm displacement, 60-2000Hz at 98.1m/s2, 30mins in 3 mutually perpendicular planes at 1 oct/min
- Solderability: MIL-STD-202, Method 208, Category 3
- Storage Temperature Range: –55 to 125°C

### **Manufacturing Details**

 Soldering: Suitable for Convection Reflow soldering. Peak temperature 260°C, 60sec max above 220°C

Washing: Able to withstand aqueous washing
 RoHS Terminations NiCoAu
 RoHS Reflow Temp 260degC 10s

## Compliance

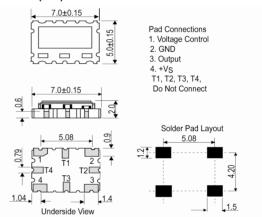
RoHS Status (2015/863/EU) CompliantREACh Status Compliant

MSL Rating (JDEC-STD-033): 1

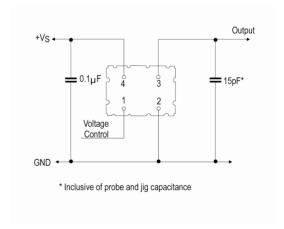




#### Outline (mm)



### **Test Circuit**



### Sales Office Contact Details:

UK: +44 (0)1460 270200 USA: +1 760 668 8935 Email: info@iqdfrequencyproducts.com Web: www.iqdfrequencyproducts.com





Part No. + Packaging: LFTVXO009920Bulk

## **Customer Part:**

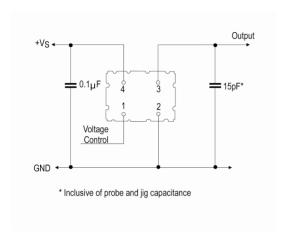
# **Packaging Details**

■ Pack Style: Bulk Loose in bulk pack

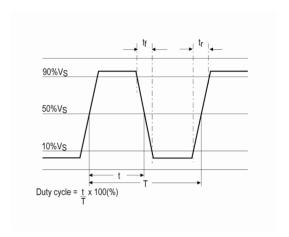
Pack Size: 100

Alternative packing option available

## **Test Circuit**



## **Wave Form**



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